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cont.

wherein the first metal gate electrode and the second metal gate electrode are each separately disposed in respective ones of the first area and the second area of the semiconductor substrate and comprise the same type of metal.

Please add the following new claims:

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18. (New) A circuit device comprising:

a first transistor including a first gate electrode over a first gate dielectric on a first area of a semiconductor substrate and having a Fermi level corresponding to a work function of one of P-type silicon and N-type silicon;

a second transistor complementary to the first transistor including a second gate electrode over a second gate dielectric on a second different area of a semiconductor substrate and having a Fermi level corresponding to a work function of the other one of P-type silicon and N-type silicon; and

wherein the first gate electrode and the second gate electrode are each separately disposed in respective ones of the first area and the second area of the semiconductor substrate and comprise the same type of material.

19. (New) The circuit device of claim 18, wherein the first gate electrode is one of a pure metal, a doped metal, and a metal alloy.

20. (New) The circuit device of claim 18, wherein the first gate dielectric is silicon dioxide.

21. (New) The circuit device of claim 18, wherein the first gate electrode is one of tantalum, tantalum nitride, molybdenum silicide, and molybdenum nitride.